

To: Technology Center: 2183
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30559

Laurence R. Simar, Jr., et al.

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman

Filed: October 31, 2000

Conf. No.: 9784

For: Very Long Instruction Word Microprocessor With Execution Packet Spanning Two or
More Fetch Packets With Pre-Dispatch Instruction Selection From Two Latches According to Instruction
Bit (amended title)

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent
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Robin E. Bamum

September 30, 2005

Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input type="checkbox"/> AMENDMENT <u>116 #2 (10 Pages)</u>
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT <u>(# Page)</u>
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
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<input type="checkbox"/> CONTINUATION APPN (# Pages)	
<input type="checkbox"/> DIVISIONAL APPN	
NAME OF INVENTOR(S):	
Laurence R. Simar, Jr., et al.	
TITLE OF INVENTION: Very Long Instruction Word Microprocessor With Execution Packet Spanning Two or More Fetch Packets With Pre- Dispatch Instruction Selection From Two Latches According to Instruction Bit (amended title)	
TI FILE NO.:	DEPOSIT ACCT. NO.:
TI-30559	20-0668
FAXED: 9/30/05	
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RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Simar et al

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman

Filed: October 31, 2000

Docket: TI-30556

For: VERY LONG INSTRUCTION WORD MICROPROCESSOR WITH EXECUTION
PACKET SPANNING TWO OR MORE FETCH PACKETS WITH PRE-DISPATCH
INSTRUCTION SELECTION FROM TWO LATCHES ACCORDING TO
INSTRUCTION BIT (amended title)

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FAX TRANSMITTAL
UNDER 37 C.F.R. §1.6(b)

I hereby certify that the above correspondence is being facsimile transmitted to the Patent and Trademark Office on September 30, 2005.


Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated August 3, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows:

In the Title of the Invention

Please change the Title of the Invention to read --VERY LONG INSTRUCTION WORD MICROPROCESSOR WITH EXECUTION PACKET SPANNING TWO OR MORE FETCH PACKETS WITH PRE-DISPATCH INSTRUCTION SELECTION FROM TWO LATCHES ACCORDING TO INSTRUCTION BIT--.